

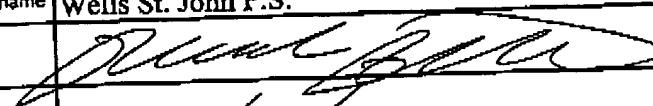
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PTO/SB/21 (6-98)
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/810,595
	Filing Date	March 15, 2001
	First Named Inventor	Belford T. Coursey
	Group Art Unit	2813
	Examiner Name	Yennhu B. Huynh
Total Number of Pages in This Submission	Attorney Docket Number MI22-1660	

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Mark S. Matkin, Reg. No. 32,268 Wells St. John P.S.
Signature	
Date	9/25/03

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/810,595
Confirmation No. 8140
Filing Date March 15, 2001
Inventor Belford T. Coursey
Assignee Micron Technology, Inc.
Group Art Unit 2813
Examiner Yennhu B. Huynh
Attorney's Docket No. MI22-1660
Customer No. 021567
Title: Memory Circuitry With Plurality of Capacitors Received Within an
Insulative Layer Well

RULE 312 RESPONSE

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VIA FACSIMILE

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated July 15, 2003, Applicant
remarks as follows:

REMARKS

Claims 26-30, 32-37 and 48-58 remain in the application.

There remains prior art which the undersigned properly submitted on a
Form PTO-1449 (1449) which accompanied a Supplemental Information
Disclosure Statement filed on or about June 24, 2002. An art reference on

MI22\1660\M07.doc